WEST Search History

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DATE: Friday, June 16, 2006

Hide?	Set Name	Query	Hit Count
		DB=PGPB,USPT; PLUR=NO; OP=ADJ	
☐	L69	l61 and L66	1
☐	L68	l61 and L67	0
☐	L67	708/670.ccls.	223
☐	L66	712/221.ccls.	367
☐	L65	(instruction$1 or L63 or L64) with L61	29
		DB=USPT,PGPB; PLUR=NO; OP=ADJ	
☐	L64	macroinstruction$1 or macro-instruction$1 or macrocode or macro-code or macro-operation$1 or macrooperation$1 or macro-op$1 or macroop$1 or macro-program$1 or macroprogram$1 or cisc instruction	1617
☐	L63	firmware or nanoinstruction$1 or nano-instruction$1 or nanocode or nano-code or nanoprogram or nano-program or L62	80217
☐	L62	microinstruction$1 or micro-instruction$1 or microcode or micro-code or micro-operation$1 or microoperation$1 or micro-op$1 or microop$1 or micro-program$1 or microprogram$1	18085
		DB=PGPB,USPT; PLUR=NO; OP=ADJ	
☐	L61	add$5 with packet$1 with carry	729
☐	L60	L27 with "carry in"	0
		DB=USPT; PLUR=NO; OP=ADJ	
☐	L59	L27 with "carry in"	0
☐	L58	(atm or packet$1) and L57	63
☐	L57	(add or added or adding or addition or adder) with (immediate adj (operand$1 or value$1))	239
		DB=PGPB,USPT; PLUR=NO; OP=ADJ	
☐	L56	(atm or packet$1) and L55	100
☐	L55	(add or added or adding or addition or adder) with (immediate adj (operand$1 or value$1))	377
		DB=USPT; PLUR=NO; OP=ADJ	
☐	L54	US-5210835-A.did.	1
☐	L53	US-4941085-A.did.	1
		DB=EPAB,JPAB,DWPI,TDBD; PLUR=NO; OP=ADJ	
☐	L52	(add or added or adding or addition or adder) and (immediate adj (operand$1 or value$1))	29
☐	L51	(add or added or adding) and (immediate adj (operand$1 or value$1))	21

☐	L50	(add or added or adding) with (immediate adj (operand$1 or value$1))	7
		DB=PGPB,USPT; PLUR=NO; OP=ADJ	
☐	L49	packet$1 and L48	1
☐	L48	atm and L47	3
☐	L47	(add or added or adding) with (immediate adj (operand$1 or value$1))	300
☐	L46	L45 not L34	81
☐	L45	L27 with (immediate or constant$1)	342
☐	L44	packet$1 and L43	24
☐	L43	start bit$1 with operand$1	33
☐	L42	L30 with operand$1	0
☐	L41	L37 with operand$1	5
☐	L40	L38 and L39	99
☐	L39	L35 same L27	12119
☐	L38	L35 and L37	99
☐	L37	marker bit$1	864
☐	L36	L30 and L35	5
☐	L35	L27 not L34	12119
☐	L34	packet with address$3	32038
☐	L33	L27 same L30	1
☐	L32	L27 and L30	39
☐	L31	L28 and L30	0
☐	L30	bit marker$1	250
☐	L29	start bit$1 and L28	1
☐	L28	operand$1 with L27	86
☐	L27	packet with add$5	41400
☐	L26	baum.in. and apple.asn.	4
☐	L25	baum.in. and apple.asn. and multi-guage.ti.	0
☐	L24	6047304.pn.	1
		DB=USPT; PLUR=NO; OP=ADJ	
☐	L23	bit field arithmetic	2
☐	L22	120 and L19	1
☐	L21	(add or added or adding or adder or adds) and L19	3
☐	L20	(add or added or adding or adder or adds) same L19	0
☐	L19	6047304.uref.	3
☐	L18	(add or added or adding or adder or adds) same L10	3
☐	L17	(add or added or adding or adder or adds) same L11	1
☐	L16	L11 not L15	25
☐	L15	L11 not L14	69

⌐	L14	packet$1 and L11	25
⌐	L13	214 and L12	1
⌐	L12	packet$1 same L11	1
⌐	L11	dedicated logic circuitry	94
⌐	L10	dedicated logic device$1	23
⌐	L9	packet$1 same L8	1
⌐	L8	L7	718

DB=PGPB,USPT; PLUR=NO; OP=ADJ

⌐	L7	dedicated with logic device$1	1343

DB=EPAB,JPAB,DWPI,TDBD; PLUR=NO; OP=ADJ

⌐	L6	(carry save add$2) with instruction$1	4
⌐	L5	L4	0

DB=PGPB,USPT; PLUR=NO; OP=ADJ

⌐	L4	(carry save add$2) with instruction$1	33
⌐	L3	carry save add$2 instruction$1	0
⌐	L2	carry save add instruction	0
⌐	L1	5838960.pn.	1

END OF SEARCH HISTORY